ET-FPGA-B
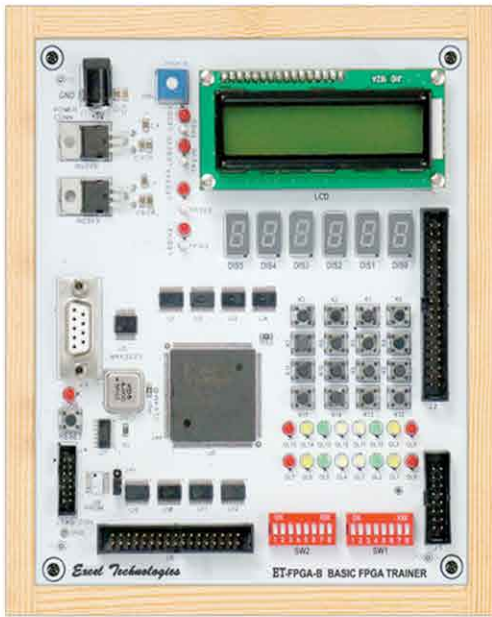
EXPERIMENTS
- 8:3 encoder, flip flop
- 2 - bits comparator, 4 - bits arithmetic logic unit
- Binary to gray code converter
- LCD controller
- Implementing UART functionality

Note : JTAG to USB Programming Cable for Xilinx has to be purchased separately

FPGA BASIC TRAINER

FEATURES
- A board that uses a SPARTAN-3 FPGA series device
- FPGA-B prototype board provides easy to use development platform
- Useful to physically verify simple digital designs using FPGA

SPECIFICATIONS

Basic functions
- FPGA : Spartan-XC3S400 in PQ208 pin package
- Serial interface : One RS232 channel using MAX3223
- Free I/Os : Available on FRC Connector
- Clock oscillator : 4MHz (compatible up to 40MHz)
- Configuration through : JTAG port

User interface
- 16 digital output LEDs
- 4 X 4 matrix keyboard
- Six 7 segment display
- 16 input switches

Note : Specifications are subject to change due to our constant efforts for Improvement. Please refer to quotation for final specifications.