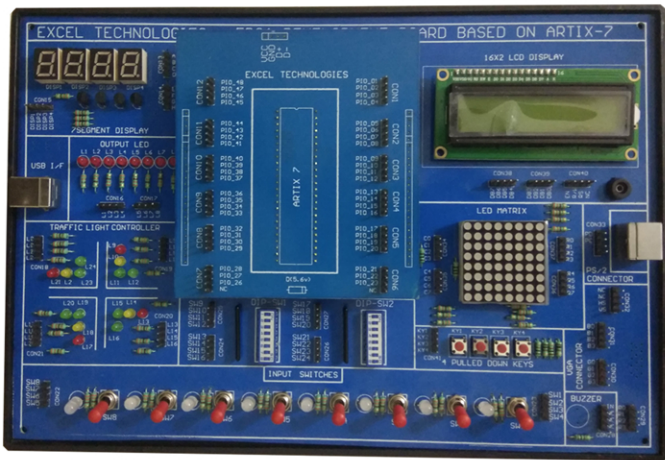




# FPGA DEVELOPMENT BOARD BASED ON ARTIX 7



MODEL : ET-FPGA-A7

## FPGA DEVELOPMENT BOARD BASED ON ARTIX 7

The Excel ET-FPGA-A7 is a user friendly FPGA development board based on Xilinx Artix-7 FPGA. The board includes a, USB-UART bridge, clock source, SRAM, Quad-SPI Flash, basic I/O devices and peripherals like LEDs, Switch, LCD, buzzer, etc. These components make it a formidable platform for digital logic circuits and MicroBlaze embedded soft-core processor designs using Xilinx's development software, Vivado. There are 44 Digital FPGA I/O signals and two FPGA Analog inputs .

### Specification :

#### \* System Features:

- Based on Xilinx Artix7 ( XC7A35T-1CPG236C)
- 1 MSPS On-chip ADC
- Quad-SPI Flash Programming
- 20,800 Look-up Tables (LUTs):
- 41,600 Flip Flops
- 225 KB Block RAM
- 5 Clock Management Tiles
- 512KB SRAM with an 8-bit bus and 8ns access times
- 4MB Quad-SPI Flash
- 48-pin DIP connector with 44 Digital I/O and 2 Analog inputs (0-3.3V)
- Powered from USB or external power Supply (AC Mains)

Note : Specifications are subject to change due to our constant efforts for Improvement. Please refer to quotation for final specifications.

#### \* System Connectivity:

- USB-UART bridge ( using USB A to B Cable)

#### \* Onboard Peripherals:

- 8 Digital (3.3V) input Toggle Switch with **Dual** Colour LED (for both High and Low Indication)
- 16 Digital (3.3V) input through 2 DIP Switches
- 4 Pulled Down Push Buttons
- 4 Multiplexed 7 Seven Segment Display
- 8 Output LEDs(RED)
- Traffic Light controller with 16 LEDs
- 8x8 LED Matrix (DOT LEDs)
- 16x2 Alphanumeric LCD
- Buzzer
- VGA Connector
- PS2 Connector

\*Enclosed in an attractive ABS plastic box with cover

\* Built in Power Supply

\*Supplied with connecting USB Cable ( A to B) and connecting leads

\* Link for User manual, Sample programs , Data Sheets & videos shared with the customer