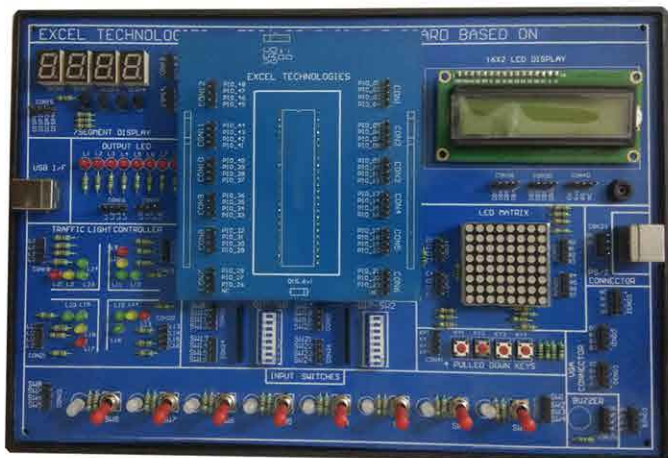




FPGA DEVELOPMENT BOARD BASED ON SPARTAN-6



MODEL : ET-FPGA-S6

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The Excel ET-FPGA-S6 is a user friendly FPGA development board based on Xilinx® Spartan®-6 LX4 FPGA. The board includes a programming ROM, clock source, USB programming, power supplies, and basic I/O devices. There are 46 FPGA I/O signals

Specification :

* System Features:

- Based on Xilinx Spartan-6 XC6SLX4-2CPG196 FPGA
- 600 slices (each with 4 LUTs and eight flip-flops)
- 8 DSP slices
- 216Kbits of block RAM
- 2 CMTs (4 DCMs and 2 PLLs)
- 16 MB Spansion® Quad SPI Flash for storing FPGA configurations and/or user data
- 46 FPGA GPIO signals brought to DIP pins
- Two on-board clock sources
- Powered from USB or external power Supply (AC Mains)

*System Connectivity:

- USB-UART bridge (using USB A to B Cable)

*Onboard Peripherals:

- 8 Digital (3.3V) input Toggle Switch with Dual Colour LED (for both High and Low Indication)
- 16 Digital (3.3V) input through 2 DIP Switches
- 4 Pulled Down Push Buttons
- 4 Multiplexed 7 Seven Segment Display
- 8 Output LEDs(RED)
- Traffic Light controller with 16 LEDs
- 8x8 LED Matrix (DOT LEDs)
- 16x2 Alphanumeric LCD
- Buzzer
- VGA Connector
- PS2 Connector

*Enclosed in an attractive ABS plastic box with cover

*Built in Power Supply

*Supplied with connecting USB Cable (A to B) and connecting leads

*Link for User manual, Sample programs , Data Sheets & videos shared with the customer

Note : Specifications are subject to change due to our constant efforts for Improvement. Please refer to quotation for final specifications.