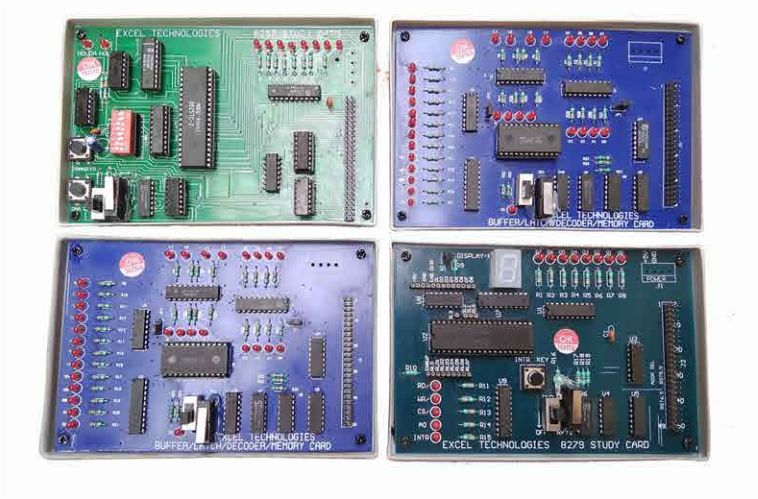
PERIPHERAL STUDY CARDS FOR TRAINING KITS



In any application based on the Microprocessor , we would find that various peripheral chips are also used. Therefore it is necessary for the students to fully understand the functional and operational aspect of the various chips available from Intel. The commonly used chips are 8255, 8251, 8253, 8279, 8257, 8155, & 8259. Apart from these chips, the students should also understand about the chips like Buffers, decoders or latches etc. The ET-Study card series is designed with that aspect in mind. The entire range of peripheral study cards are available from us.

These cards have been designed in such a way that the students can learn about the various modes in which these peripherals can be programmed.

A facility has been provided to run the programs in single I/O Instruction mode so that after every Input or Output command, the system stops as the ready signal is pulled down. The various data Lines, address lines which are used in the peripheral chip like A0 & A1 etc. or the Chip select or the control signals are indicated by corresponding LEDs.

8255 Study Card ET-8255

Incorporates INTEL 8255, Programmable Peripheral Interface Chip• 8 Digital Inputs provided using 8 way DIP Switch • 8 LED I1 to I7 are provided for indicating the High or Low input • 8 output LEDs O0 to O7 are provided for connecting to any port • 8 LEDs L0 to L7 are connected to port B through jumpers. .• The interrupts through Pc0 or PC3 of 8255 t.• LED is provided to indicate the status of the Data Bus and control signals like A0,A1, CS*, RD* and WR*• All Address, data and control lines are terminated in a 50 Pin FRC male connector to interface with bus

8253 Study Card ET-8253

• Provision for test points & fault analysis points • On board 8 Output LEDs • On board LED for RD, WR A0, A1 • Provision for Single Stepping using Cycle Switch • Output Pins provided for CLK1, CLK2 , CLK0, OUT 0, OUT1, OUT2, GATE0, GATE1, GATE2, CPU CLK • All Address, data and control lines are terminated in a 50 Pin FRC Male connector to interface with bus

8259 Study Card ET-8259

• Incorporates INTEL 8259, Interrupt Controller Chip • 8 IRQ lines terminated at a 8 Pin Header • On board 8 Output LEDs • On board LED for RD, WR ,INTA, INTR, A0 • Provision for Single Stepping using Cycle Switch • Compatible with 8085, 8088/86 CPU Based Microprocessor Kits • Provision for test points & fault analysis points • All Address, data and control lines are terminated in a 50 Pin FRC Male connector to interface with bus

8279 Keyboard and Display Study Card ET-8279

• INTEL 8279 Keyboard/Display Interface • Facility for Interrupt I/O Transfer • Provision for Single Stepping using Cycle Switch • 1 Seven Segment Display • Provision to provide simulation of 32 Keys using Jumper Wires • On board 8 Output LEDs • On board LED for RD , WR, CS, A0 and INTR • All Address, data and control lines are terminated in a 50 Pin FRC Male connector to interface with bus

8251 USART Study Card ET-8251

•Serial communication using 8251 Universal Synchronous/ Asynchronous Receiver Transmitter IC. • Output are provided on 9 pin D-Type connector• Data lines from AD0 to AD7 are indicated by 3mm LEDs. • Chip Select, Read, Write, A0, A1, DTR, DSR, RTS, CTS, TxRDY, RxRDY are indicated by 3mm LEDs•Hardware Single Step and Full Clock Execution modes are provided. • Single stepping can be performed using micro switch provided on board. • Interface 8085/8086 Kit using 50 pin FRC Connector.

Note : Specifications are subject to change due to our constant efforts for Improvement. Please refer to quotation for final specifications.

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