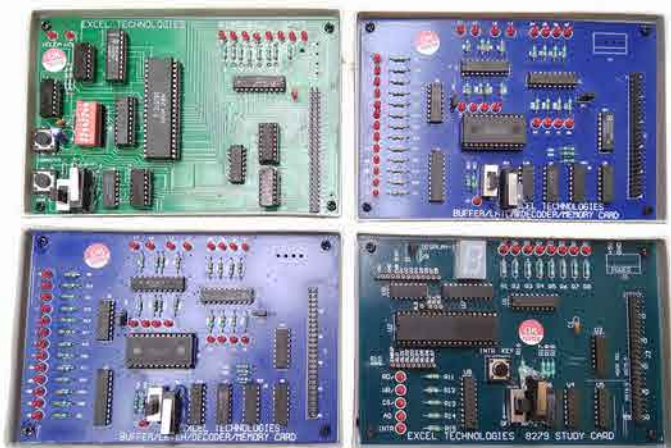




PERIPHERAL STUDY CARDS FOR TRAINING KITS



In any application based on the Microprocessor , we would find that various peripheral chips are also used. Therefore it is necessary for the students to fully understand the functional and operational aspect of the various chips available from Intel. The commonly used chips are 8255, 8251, 8253, 8279, 8257, 8155, & 8259. Apart from these chips, the students should also understand about the chips like Buffers, decoders or latches etc. The ET-Study card series is designed with that aspect in mind. The entire range of peripheral study cards are available from us.

These cards have been designed in such a way that the students can learn about the various modes in which these peripherals can be programmed.

A facility has been provided to run the programs in single I/O Instruction mode so that after every Input or Output command, the system stops as the ready signal is pulled down. The various data Lines, address lines which are used in the peripheral chip like A0 & A1 etc. or the Chip select or the control signals are indicated by corresponding LEDs.

Therefore it becomes easy for a student to see the affect of the instruction currently executed by the processor.

If required, the programs can be run in full speed mode.

These cards are connected to the Kit through 50 pin connector and cable.

The various cards available from us are given below:

- 1. 8255 :** Box headers for easy connections. LED indicators for data and control signals. Data/Control signals can be seen in single stepping. Facility of setting 8255 base address. All 8255 ports on box headers. 8 LEDs for monitoring port data. Useful to study detailed 8255 operation. Useful to study Control Signals in single step. It consists of buffers, switches, de bounce kits for software Single Stepping, one 8255 with tags for all I/O ports, VCC and GND tags, LEDs to display status.
- 3. 8253:** It consists of buffers, switches, debounce circuits for software Single Stepping, one 8253 with tags for all the counters, VCC and GND tags, LEDs to display status
- 4. 8279:** It consists of buffers, switches and de-bounce circuit for software single stepping of every access to the 8279 card and LEDs to display status. Switch S1 is used to enable Single stepping or to keep CPU in free running mode.
- 5. 8251:** The operation of this USART chip can be studied. The necessary cables and the software is supplied along with the card for the interface to IBM PC/AT
- 6. 8259:** It consists of buffers, switches debounce circuits for software single stepping of every access, 2 nos. of 8259 with tags for applying Interrupts, Vcc tags, Leds to display status.

Note : Specifications are subject to change due to our constant efforts for Improvement. Please refer to quotation for final specifications.